



Patent Docket No.: Intel 2207/1012802  
Assignee: Intel Corporation

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Ronald O. Smith

SERIAL NO. OF  
PARENT APPLICATION : 10/787,232

FILING DATE OF  
PARENT APPLICATION : February 27, 2004

FOR : METHOD AND APPARATUS  
FOR IMPROVING RELIABILITY  
IN MICROPROCESSORS

ART UNIT OF  
PARENT APPLICATION : 2114

EXAMINER OF  
PARENT APPLICATION : Timothy M. Bonura

M/S: Amendments  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE**

Dear Sir:

Please enter the following amendments in this continuation application prior to  
Examination of this case.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on  
page 3 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.